

SC5-FESTIVAL • Resources w. 1+8 Slots Backplane (System Slot Left Aligned Version)

	1	2	3	4	5	6	7	8	9	
	△ SYS	○	○	○	○	○	○	○	○	
P6	GbE (1-8)	GbE (1)	GbE (2)	GbE (3)	GbE (4)	GbE (5)	GbE (6)	GbE (7)	GbE (8)	
P5	Clk PE (1-8) PE Gen3 (7-8)	GA 111	GA 110	GA 101	GA 100	GA 011	GA 010	GA 001	GA 000	
P4	PE Gen3 (3-6)	PER 1	PER 2	PER 3	PER 4	PER 5	PER 6	PER 7	PER 8	
P3	SATA PCH (7-8) SATA RAID (3-6) USB3 (2-3) USB2 (4-8)	CPU N/A N/A 0:1:0	CPU N/A N/A 0:1:1	CM238 HSI0-11 PCIe #5 0:28:4	CM238 HSI0-12 PCIe #6 0:28:5	CM238 HSI0-13 PCIe #7 0:28:6	CM238 HSI0-14 PCIe #8 0:28:7	CM238 HSI0-25 PCIe #19 0:27:2	CM238 HSI0-26 PCIe #20 0:27:3	
P2	PE Gen3 x 8 (2) PE Gen3 ½ x 8 (1)			PCI Express® Root Port Function Numbers may vary according to UEFI Settings at: Advanced/Advanced Menu/PCH-IO/PCI Express/PCIe function swap [Disabled] [Enabled]						
P1	PE Gen3 ½ x 8 (1) USB3 (1)	PE Gen3 x 8 USB3	PE Gen3 x 8 USB3	PE Gen3 x 1 SATA 6G (RAID) USB3	PE Gen3 x 1 SATA 6G (RAID) USB2	PE Gen3 x 1 SATA 6G (RAID) USB2	PE Gen3 x 1 SATA 6G (RAID) USB2	PE Gen3 x 1 SATA 6G (PCH) USB2	PE Gen3 x 1 SATA 6G (PCH) USB2	
	SC5-FESTIVAL	Fat Pipe Slot	Fat Pipe Slot	Peripheral Slot	Peripheral Slot	Peripheral Slot	Peripheral Slot	Peripheral Slot	Peripheral Slot	

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system slot connector assignment numbers in brackets (e.g. SATA PCH (7-8) according to the CPCI-5.0 specification table 44/45

SATA (PCH) assigned connectors are Intel CM238 Platform Controller Hub derived ports

SATA (RAID) assigned connectors are Marvell 88SE9230 hardware RAID controller derived ports (may be operated non RAID)

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	①	②	③	④	⑤	⑥	⑦	⑧	⑨
P6	GbE (8)	GbE (7)	GbE (6)	GbE (5)	GbE (4)	GbE (3)	GbE (2)	GbE (1)	GbE (1-8)
P5	GA 000	GA 001	GA 010	GA 011	GA 100	GA 101	GA 110	GA 111	Cik PE (1-8)
P4	PER 8	PER 7	PER 6	PER 5	PER 4	PER 3	PER 2	PER 1	PE Gen3 (7-8)
P3	CM238 HSIO-26 PCIe #20 0:27:3	CM238 HSIO-25 PCIe #19 0:27:2	CM238 HSIO-14 PCIe #8 0:28:7	CM238 HSIO-13 PCIe #7 0:28:6	CM238 HSIO-12 PCIe #6 0:28:5	CM238 HSIO-11 PCIe #5 0:28:4	CPU N/A N/A 0:1:1	CPU N/A N/A 0:1:0	SATA PCH (7-8) SATA RAID (3-6) USB3 (2-3) USB2 (4-8)
P2	PCI Express® Root Port Function Numbers may vary according to UEFI Settings at: Advanced/Advanced Menu/PCH-IO/PCI Express/PCIe function swap [Disabled] [Enabled]								PE Gen3 x 8 (2)
P1	PE Gen3 x 1 SATA 6G (PCH) USB2	PE Gen3 x 1 SATA 6G (PCH) USB2	PE Gen3 x 1 SATA 6G (RAID) USB2	PE Gen3 x 1 SATA 6G (RAID) USB2	PE Gen3 x 1 SATA 6G (RAID) USB2	PE Gen3 x 1 SATA 6G (RAID) USB3	PE Gen3 x 8 USB3	PE Gen3 x 8 USB3	PE Gen3 ½ x 8 (1) PE Gen3 ½ x 8 (1) USB3 (1)
	Peripheral Slot	Peripheral Slot	Peripheral Slot	Peripheral Slot	Peripheral Slot	Peripheral Slot	Fat Pipe Slot	Fat Pipe Slot	SC5-FESTIVAL

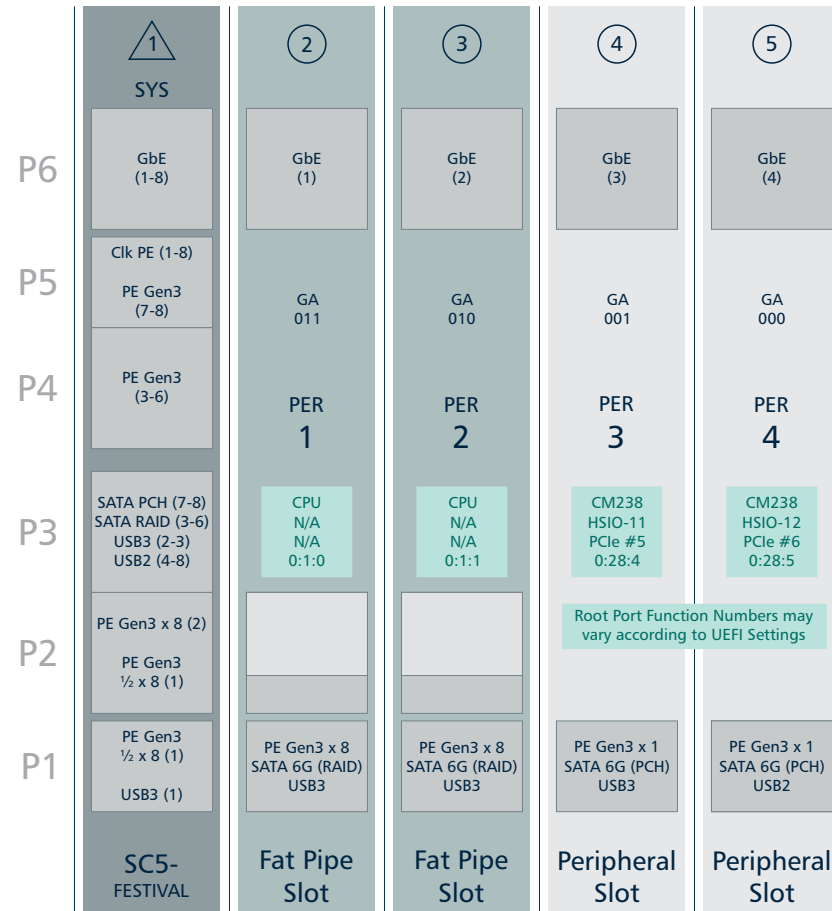
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SC5-FESTIVAL • Resources w. 1+4 Slots Backplane (System Slot Left Aligned Version)



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system slot connector assignment numbers in brackets (e.g. SATA PCH (7-8) according to the CPCI-5.0 specification table 44/45

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SC5-FESTIVAL • Resources w. 1+4 Slots Backplane (System Slot Right Aligned Version)

	①	②	③	④	⑤ SYS
P6	GbE (4)	GbE (3)	GbE (2)	GbE (1)	GbE (1-8)
P5	GA 000	GA 001	GA 010	GA 011	Clk PE (1-8) PE Gen3 (7-8)
P4	PER 4	PER 3	PER 2	PER 1	PE Gen3 (3-6)
P3	CM238 HSIO-12 PCIe #6 0:28:5	CM238 HSIO-11 PCIe #5 0:28:4	CPU N/A N/A 0:1:1	CPU N/A N/A 0:1:0	SATA PCH (7-8) SATA RAID (3-6) USB3 (2-3) USB2 (4-8)
P2	Root Port Function Numbers may vary according to UEFI Settings				PE Gen3 x 8 (2) PE Gen3 ½ x 8 (1)
P1	PE Gen3 x 1 SATA 6G (PCH) USB2	PE Gen3 x 1 SATA 6G (PCH) USB3	PE Gen3 x 8 SATA 6G (RAID) USB3	PE Gen3 x 8 SATA 6G (RAID) USB3	PE Gen3 ½ x 8 (1) USB3 (1)
	Peripheral Slot	Peripheral Slot	Fat Pipe Slot	Fat Pipe Slot	SC5-FESTIVAL

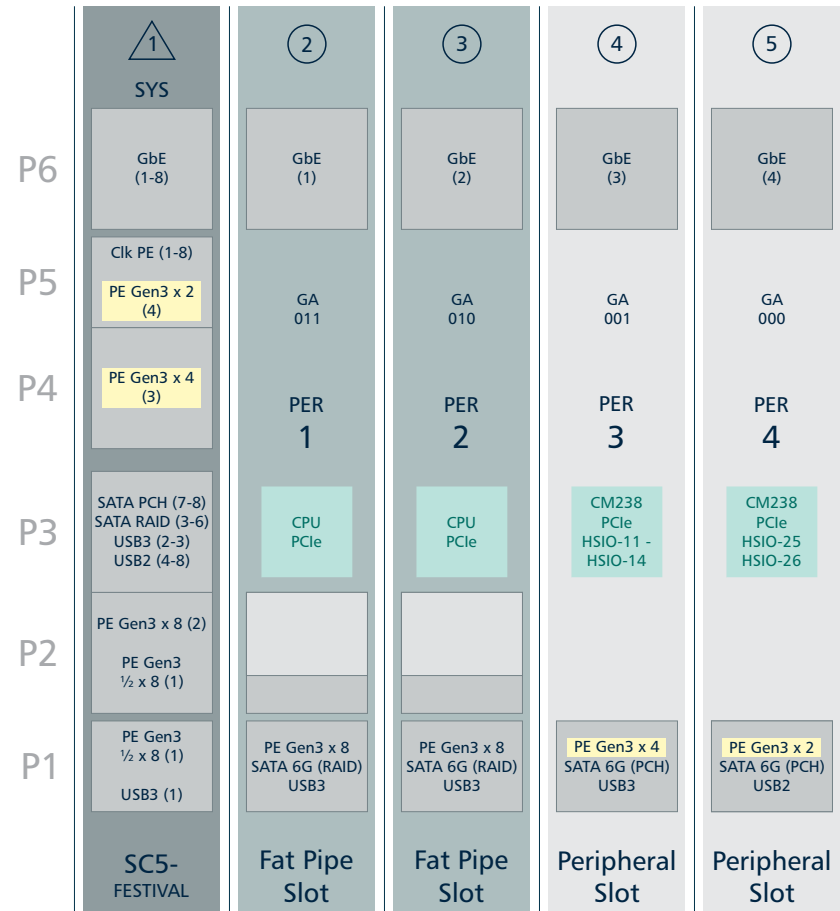
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SC5-FESTIVAL • Resources w. 1+4 Slots **Sample Custom Specific Backplane**



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Highlighted: Non-standard backplane routing PCIe Gen3 link width 8-8-4-2
 Other link width configurations possible e.g. 8-8-4-1-1 or 8-8-2-2-2
 Either system slot left, or system slot right
 Option 8HP pitch for PCIe x8 slots for GPU card usage